KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE
(An Autonomous Institute under Kakatiya University, Warangal)
Opp : Yerragattugutta, Bheemaram (V), Hasanparthy (Mandal), WARANGAL - 506 015, Telangana State, INDIA.
काकतीय प्रौद्योगिकी एवं विज्ञान संस्थान, वरंगल - ५०६ ०१५ तेलंगाना राज्य, भारत
కాకతీయ సాంకేతిక విజ్ఞాన శాస్త్ర విద్యాలయం, వరంగల్ - 506 015. తెలంగాణ రాష్ట్రం. భారతదేశము
Office of the Controller of Examinations

for Supplementary Students

2024 Admitted Batch only

No. ESE Schedule Supply /B.Tech. III-Sem (URR24)/2026/279

Date: 12-11-2026

END SEMESTER EXAMINATIONS (ESE) FOR B.TECH. III-SEMESTER (URR24) (SUPPLEMENTARY) NOVEMBER/DECEMBER -2026

TIME – TABLE

Examination Timings: FN: 10.00 am to 01.00 pm

Date & Day of Examination	CE	ME	CSE	EEE	ECE	IT	CSE(N)	ECI	CSE (IoT)	CSE (AIML)	CSE (DS)
18.11.2026 Wednesday	Numerical and Statistical Methods	Applied Mathematics	Software Engineering	Applied Mathematics	Applied Mathematics	Software Engineering	--	Mathematical foundations for Signal Processing	Fundamentals of IoT	Essential Mathematics and Statistics for Machine Learning	Essential Mathematics and Statistics for Data science
20.11.2026 Friday	Mechanics of Materials	Mechanics of Materials	Advanced Data Structures	Network Analysis	Electronics Circuits Analysis and Design	Advanced Data Structures	Advanced Data Structures	Sensors and Actuators	Advanced Data Structures	Advanced Data Structures	Advanced Data Structures
25.11.2026 Wednesday	Surveying	Manufacturing Technology	Theory of Computation	Digital Electronics & Operational Amplifiers	Signals and Systems	Artificial Intelligence	Operating Systems	Analog Integrated Circuits and Applications	Operating Systems	Operating Systems	Operating Systems
27.11.2026 Friday	Construction Materials	Heat Power Engineering	Computer Networks	DC Machines & Transformers	Digital Design	Database Management Systems	Computer Networks	Digital Circuits and Logic Design	Computer Networks	-	-
30.11.2026 Monday	Python Programming	Python Programming	Object Oriented Programming through JAVA	Object Oriented Programming through Java	Object Oriented Programming through Java	Object Oriented Programming through Java	Object Oriented Programming through Java	Object Oriented Programming through Java	Object Oriented Programming through Java	Object Oriented Programming through Java	Object Oriented Programming through Java
02.12.2026 Wednesday	Quantitative Aptitude and Logical Reasoning	--	Quantitative Aptitude and Logical Reasoning	Quantitative Aptitude and Logical Reasoning	Quantitative Aptitude and Logical Reasoning	--	Automata Theory and Compiler Design	Quantitative Aptitude and Logical Reasoning	--	Automata Theory and Compiler Design	Automata Theory and Compiler Design

Candidate should occupy his/her seat at least **10 minutes before** the commencement of the examination.

To: Exam Branch Notice Boards

Copy to: 1. Principal *for favour of information* 2.Dean, Academic Affairs 3. HOD's- with a request to display in the Dept Notice Boards & share in the Class WhatsApp groups
4. Training & Placement Officer 5. Web Team- with a request to display on the kitsw.ac.in/examinations/Examinations_schedules.html

Controller of Examinations